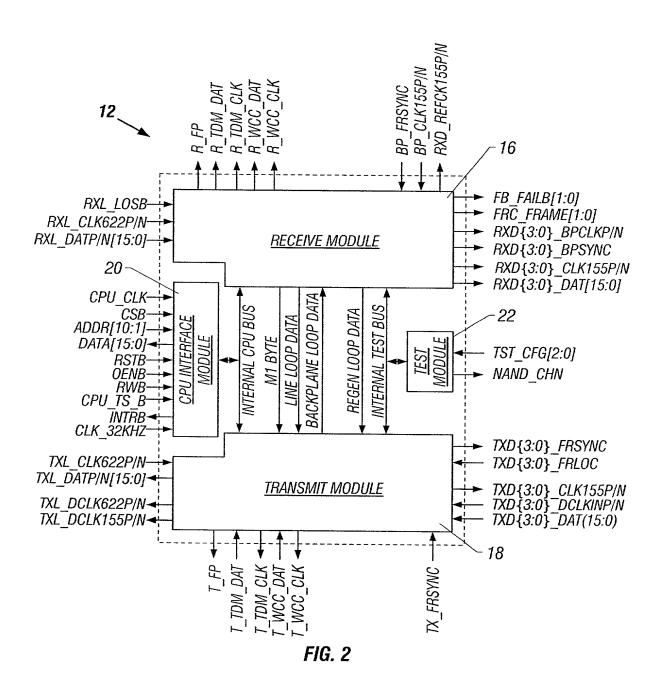
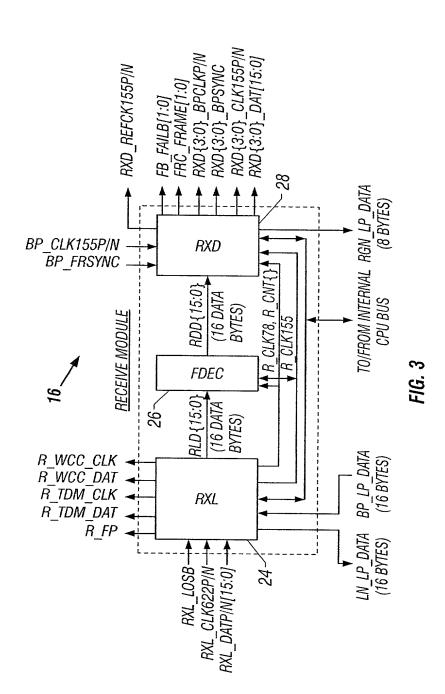


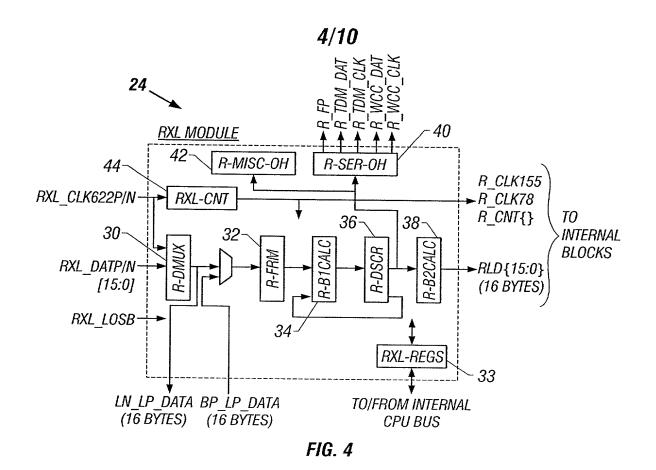
FIG. 1

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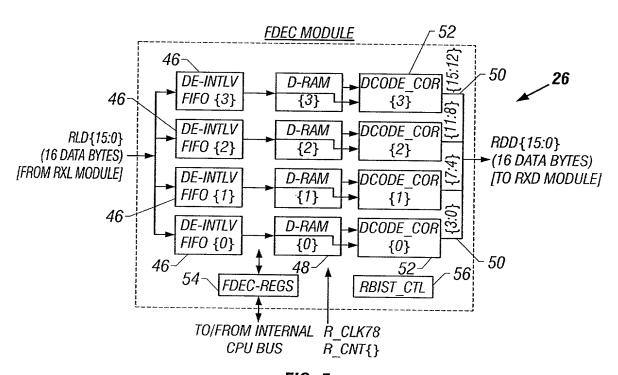


FIG. 5

Title: Error Insertion Circuit for SONET Forward Error Correction

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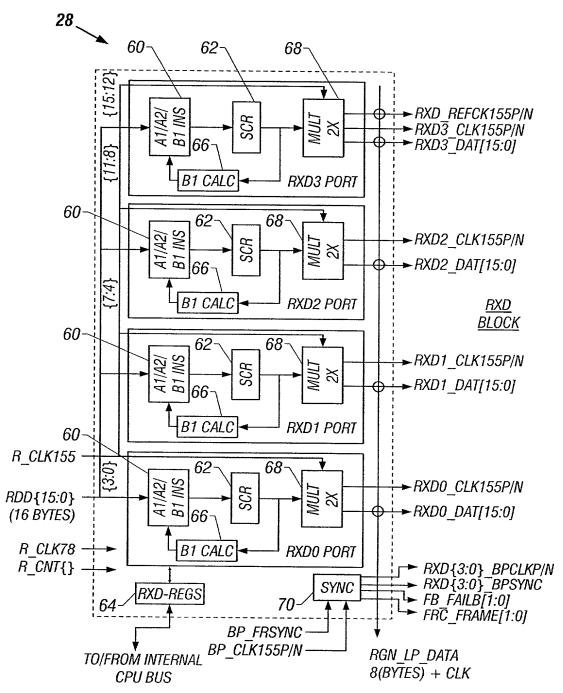
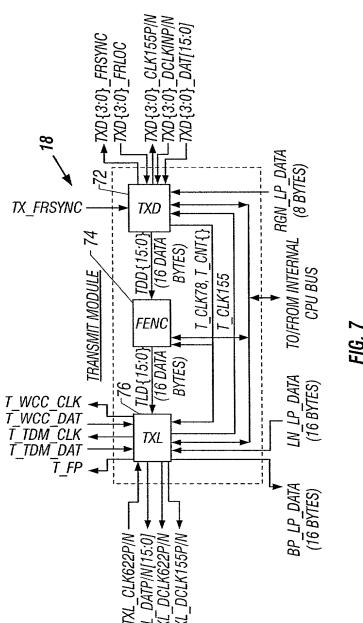


FIG. 6

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Title: Error Insertion Circuit for SONET Forward Error Correction

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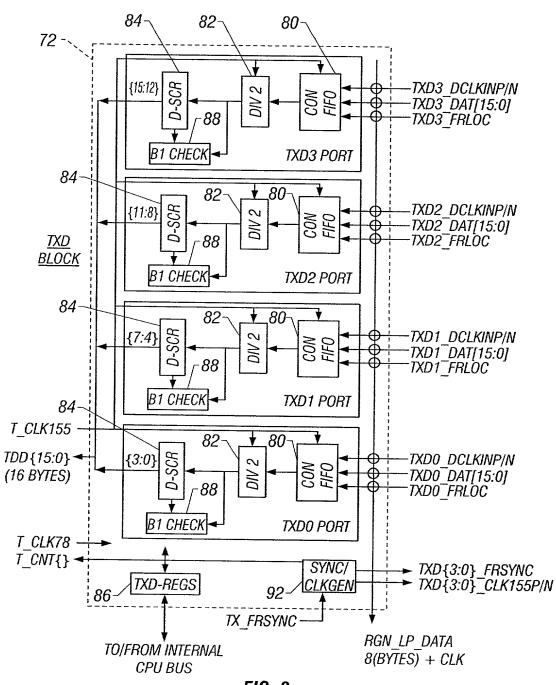
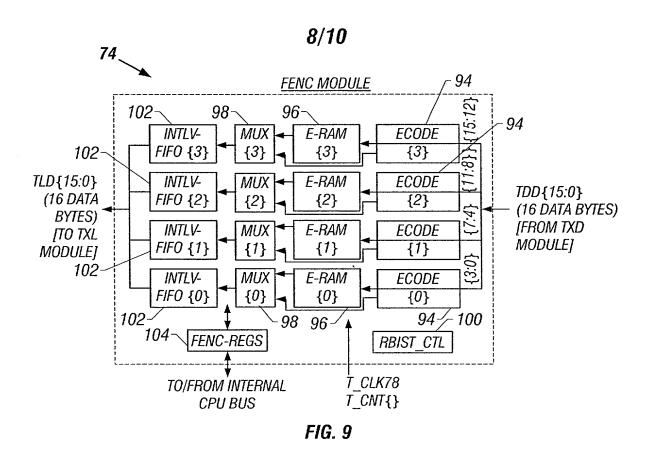
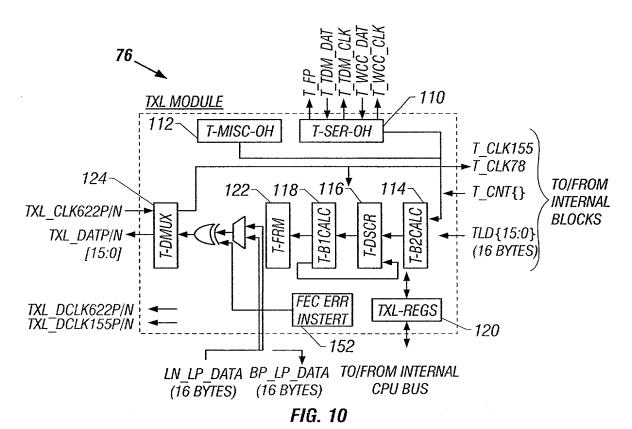
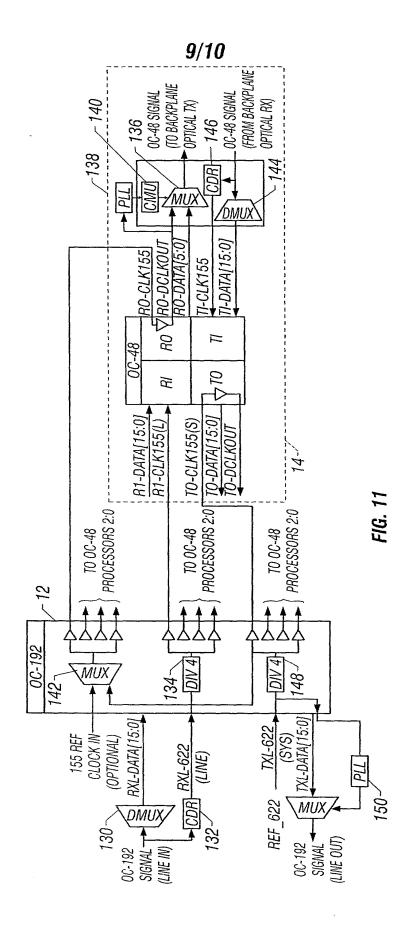


FIG. 8







Title: Error Insertion Circuit for SONET Forward Error Correction

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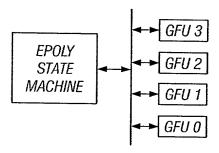


FIG. 12

